

FIG. 1

122
MEMORY

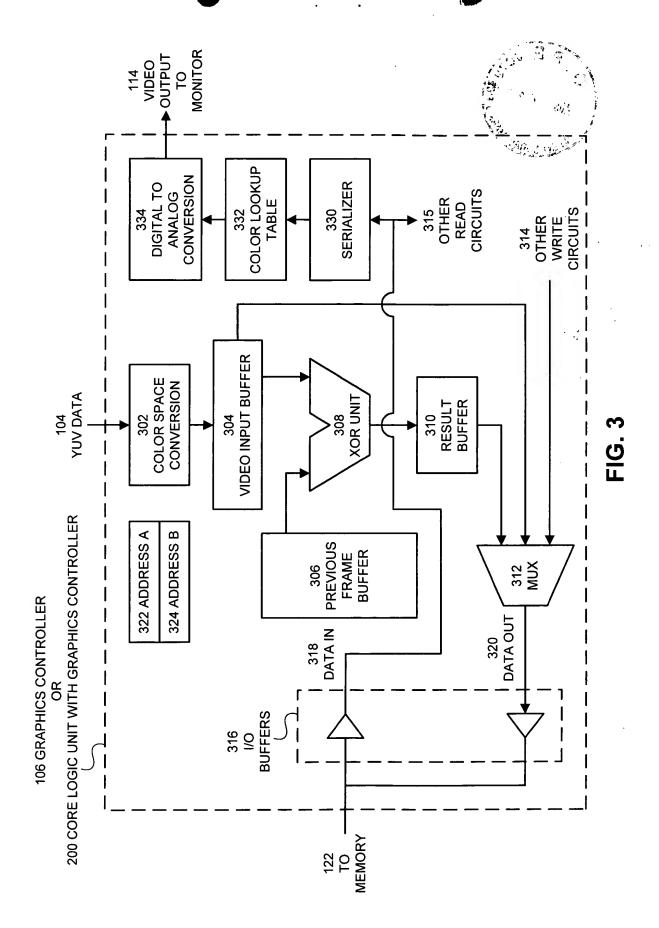
OCORE LOGIC UNIT WITH
GRAPHICS
CONTROLLER

114
VIDEO
OUTPUT TO
MONITOR

116 BUS

116 BUS

FIG. 2



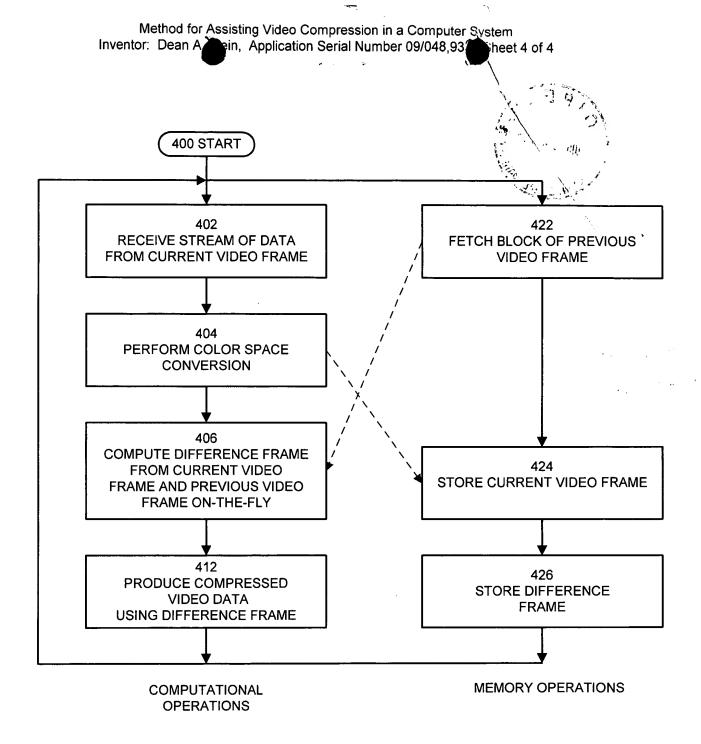


FIG. 4